

Fig. 1A

(PRIOR ART)

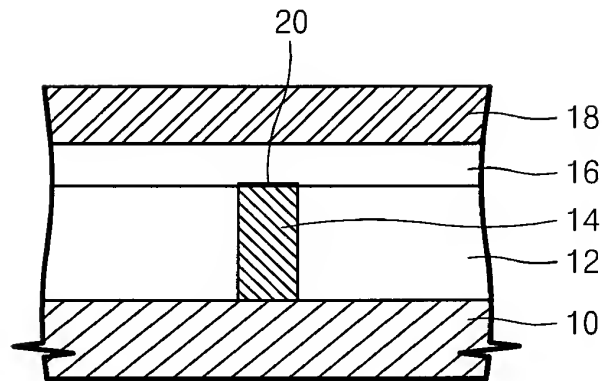


Fig. 1B

(PRIOR ART)

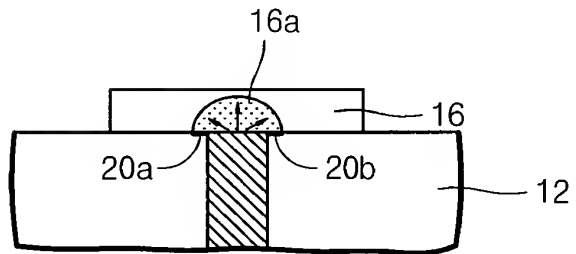


Fig. 2A

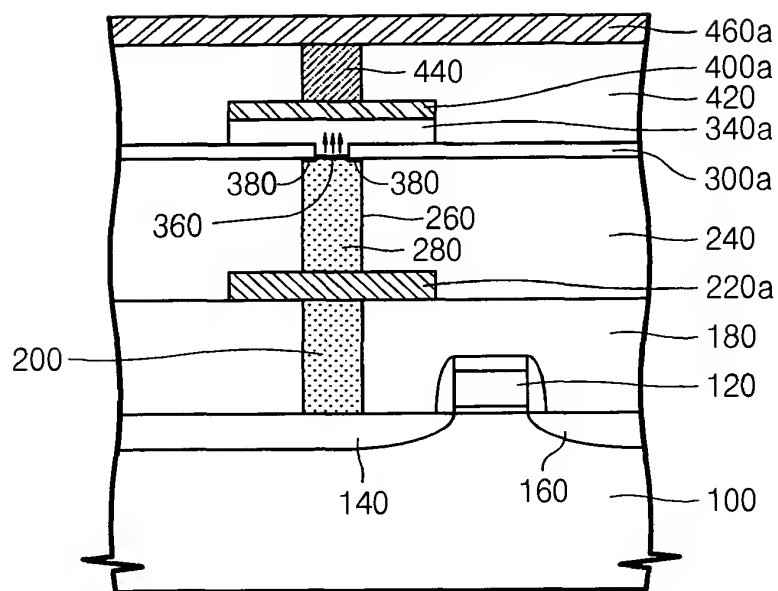


Fig. 2B

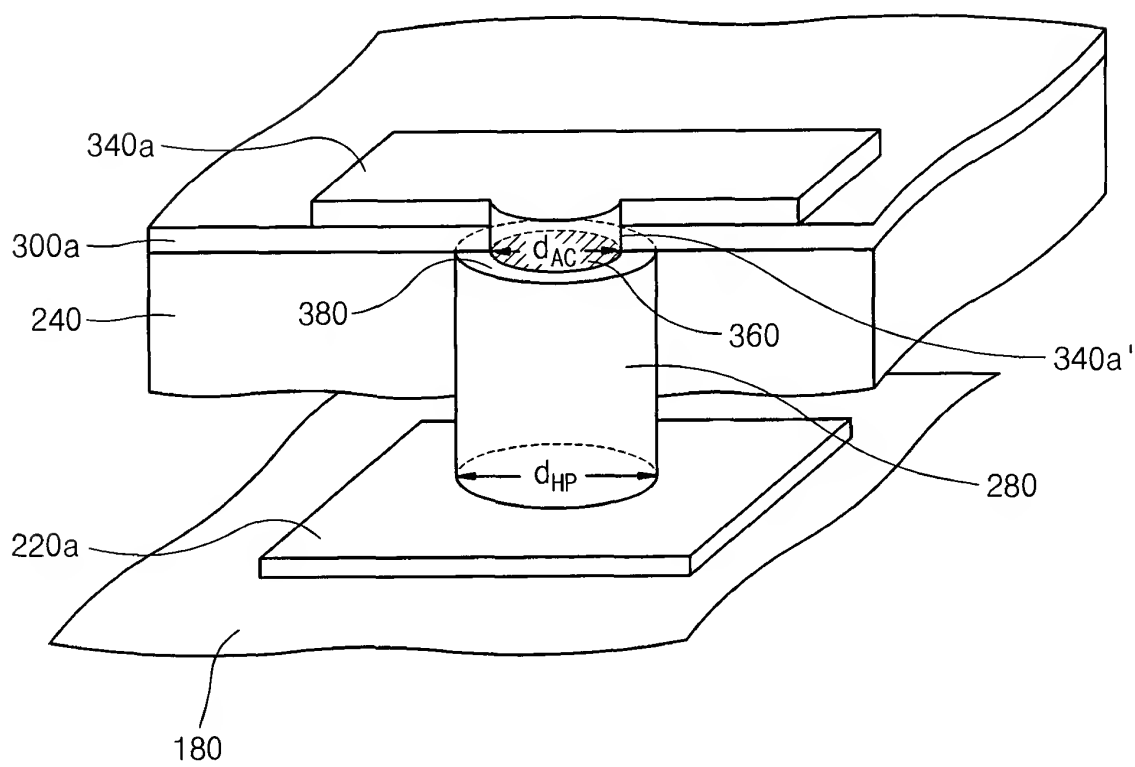


Fig. 3

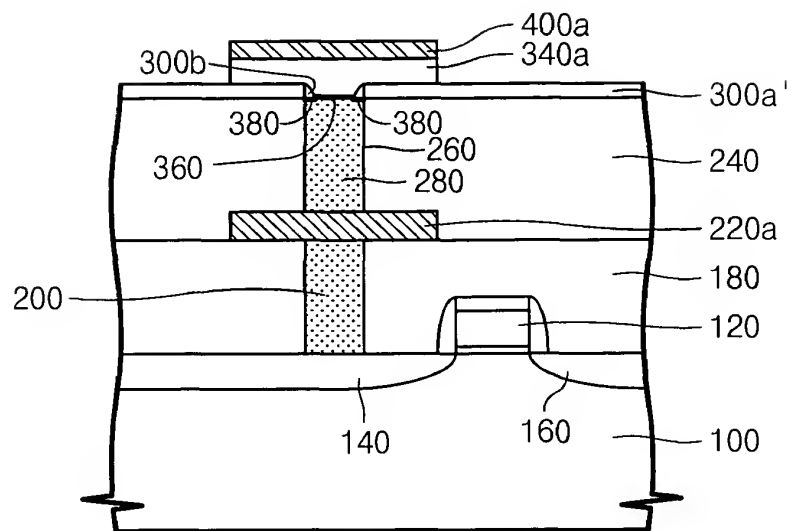


Fig. 4

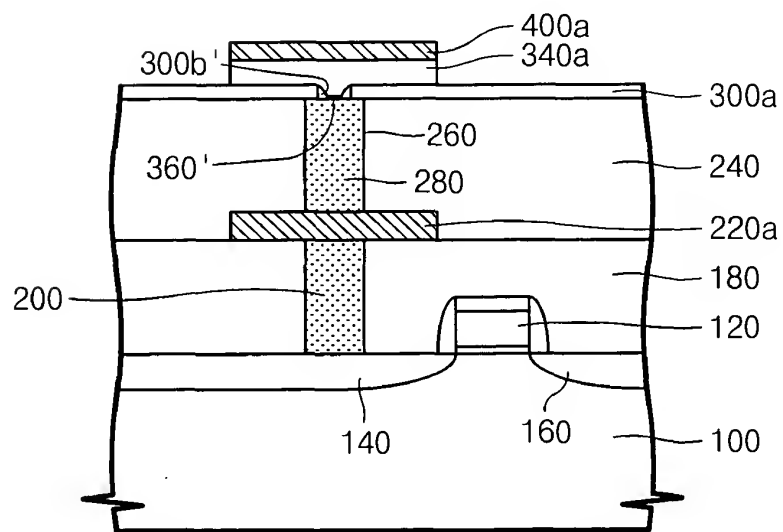


Fig. 5

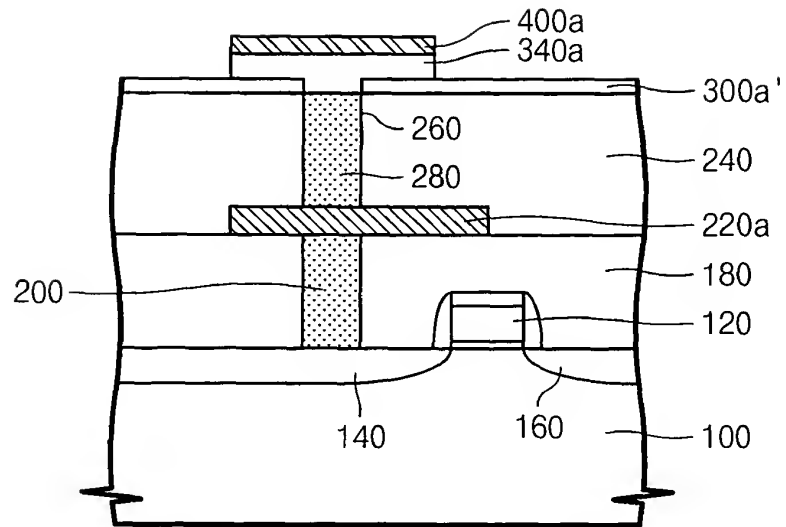


Fig. 6

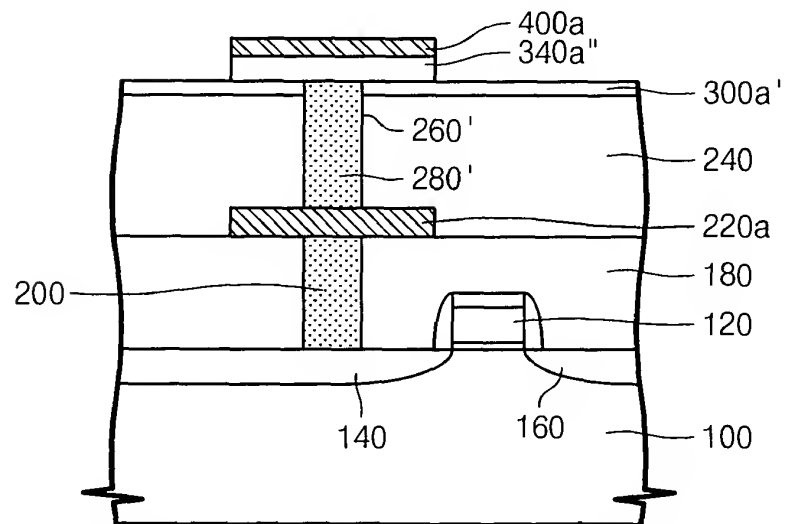


Fig. 7A

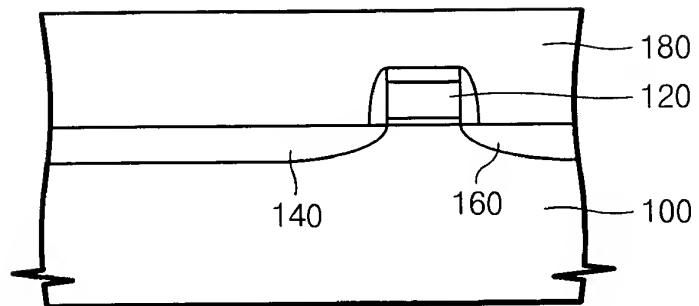


Fig. 7B

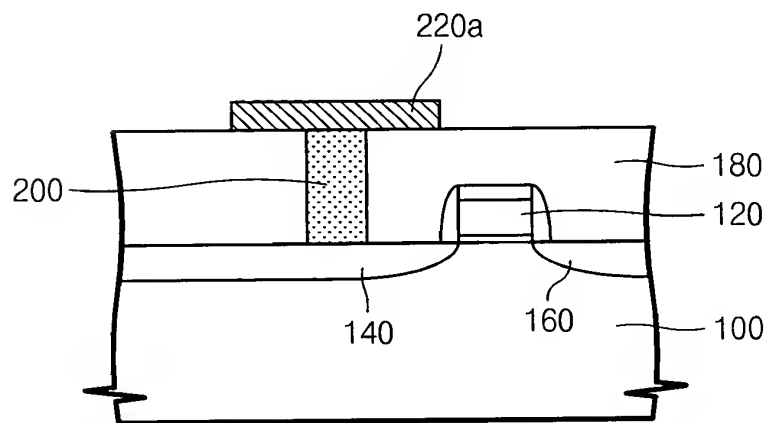


FIG. 1 is a cross-sectional view of a semiconductor device. The device includes a substrate 100 with a well 140 formed in it. A gate stack 120 is formed on the surface of the substrate 100. A source/drain region 160 is formed in the substrate 100, adjacent to the gate stack 120. A conductive layer 200 is formed on the surface of the substrate 100, covering the gate stack 120 and the source/drain region 160. A second conductive layer 220a is formed on top of the conductive layer 200. A contact plug 240 is formed in the second conductive layer 220a, extending through the conductive layer 200 to the source/drain region 160. A third conductive layer 260 is formed on top of the second conductive layer 220a, covering the contact plug 240 and the source/drain region 160.

A cross-sectional view of a container 100. A central vertical member 280 is positioned within the container. A horizontal member 220a is positioned across the container, intersecting the vertical member 280. The horizontal member 220a is shown with a hatched pattern. The vertical member 280 is shown with a dotted pattern. The container 100 has a curved side wall 240. A horizontal line 180 is shown below the horizontal member 220a. A horizontal line 140 is shown below the horizontal line 180. A horizontal line 160 is shown below the horizontal line 140. A horizontal line 120 is shown below the horizontal line 160. A horizontal line 200 is shown below the horizontal line 120. A horizontal line 260 is shown below the horizontal line 200.

Fig. 7E

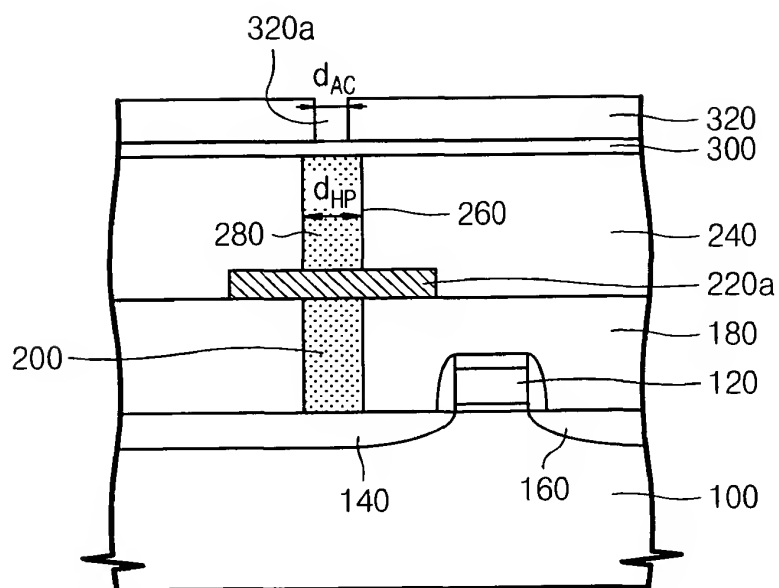


Fig. 7F

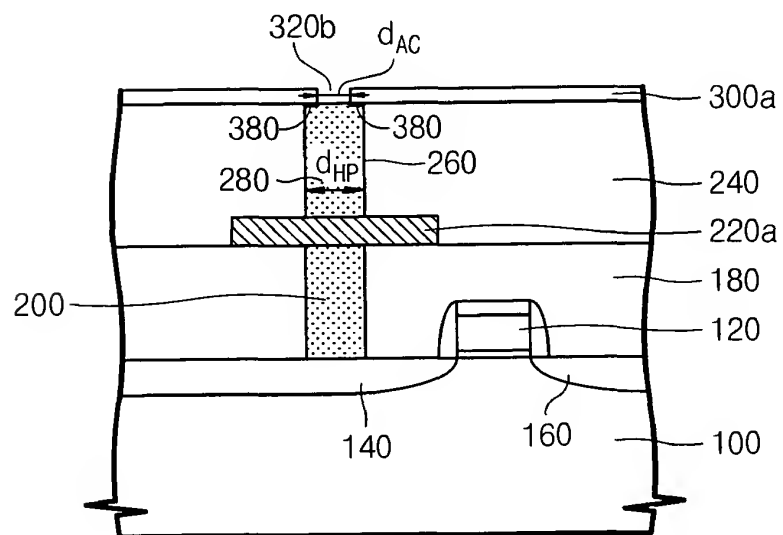


Fig. 7G

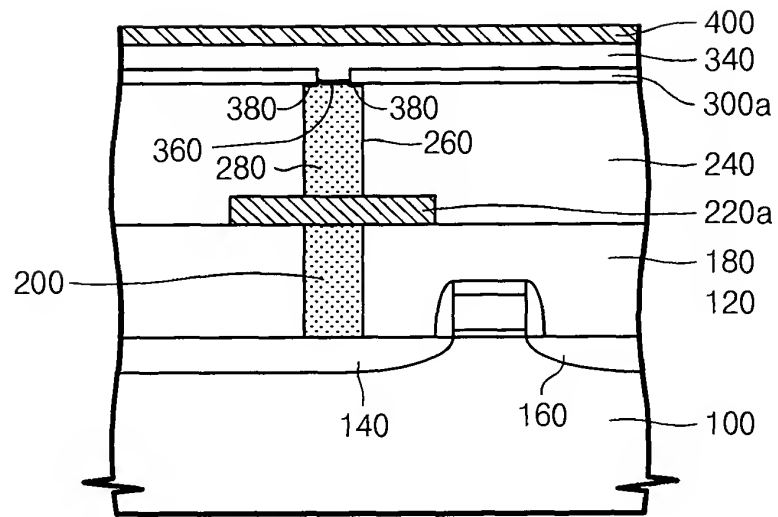


Fig. 7H

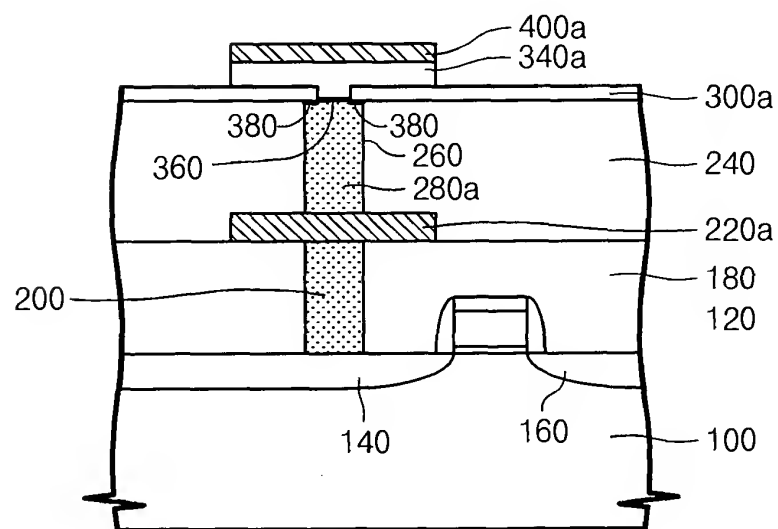




Fig. 7I

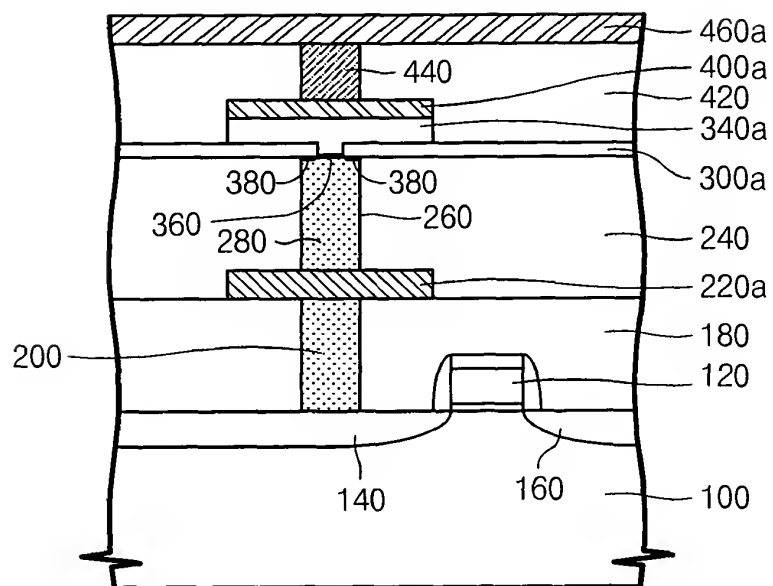


Fig. 8A

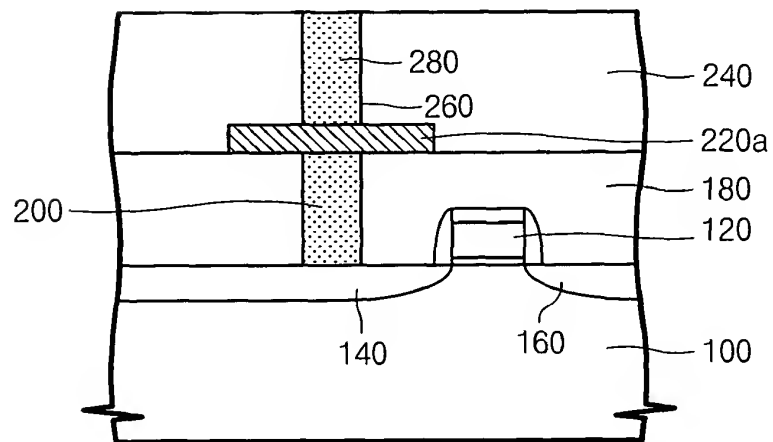


Fig. 8B

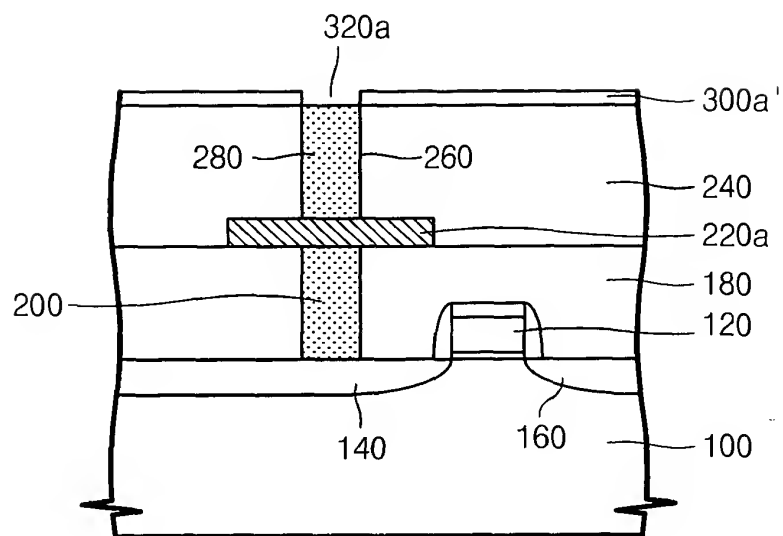


Fig. 8C

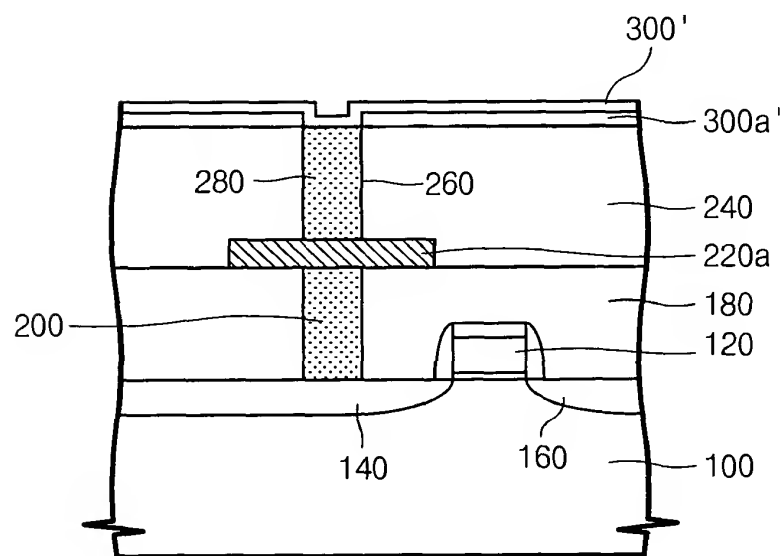


Fig. 8D

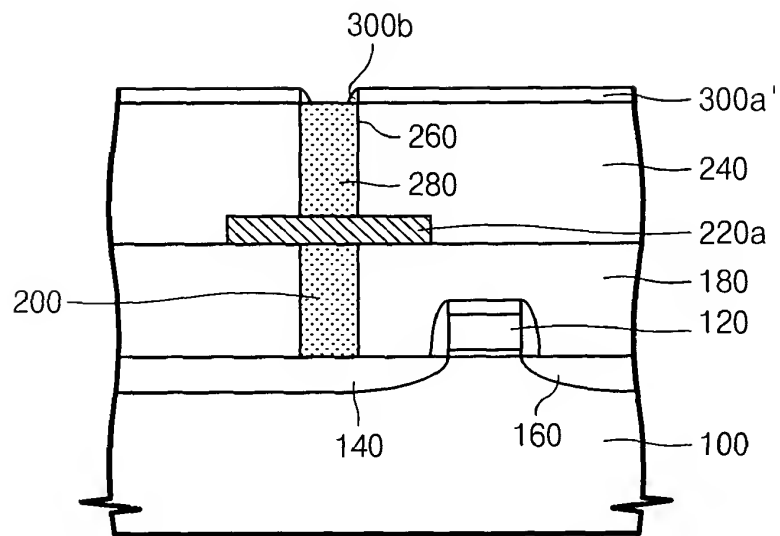


Fig. 8E

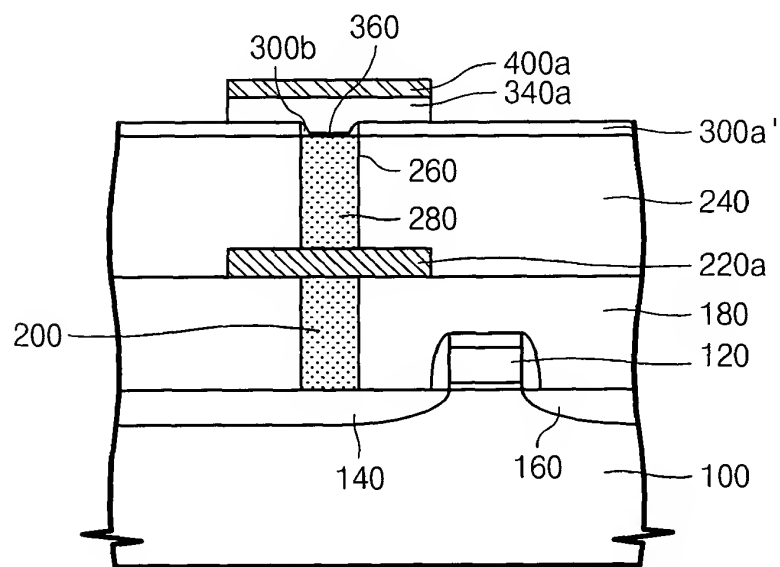


Fig. 9A

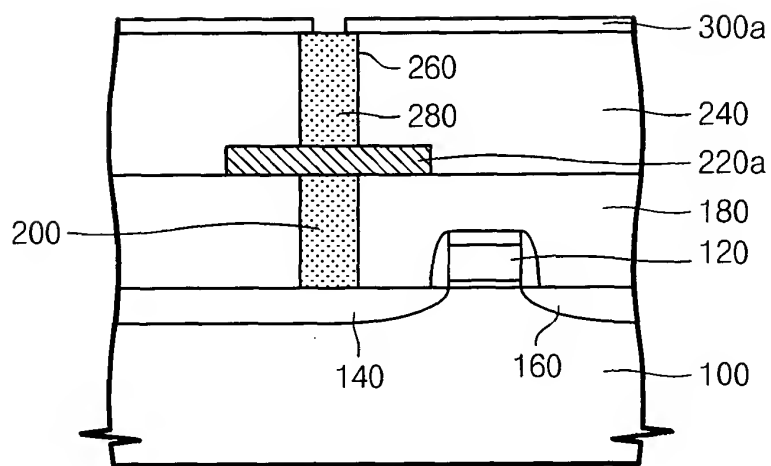


Fig. 9B

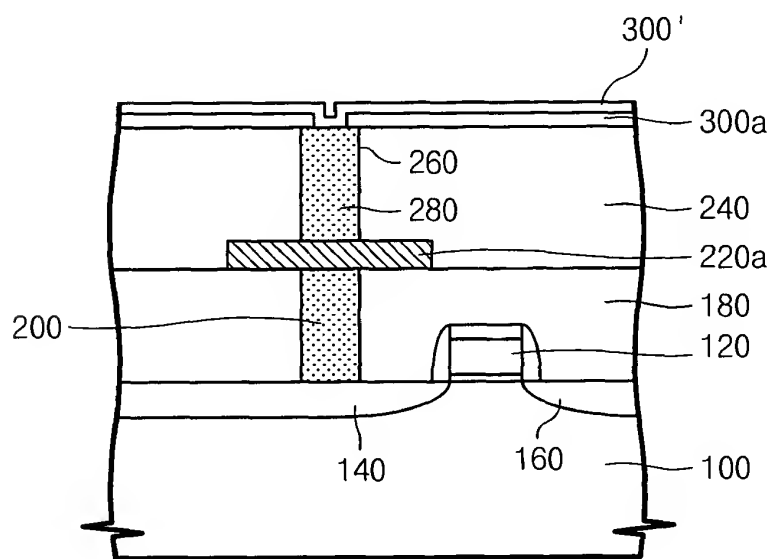


Fig. 9C

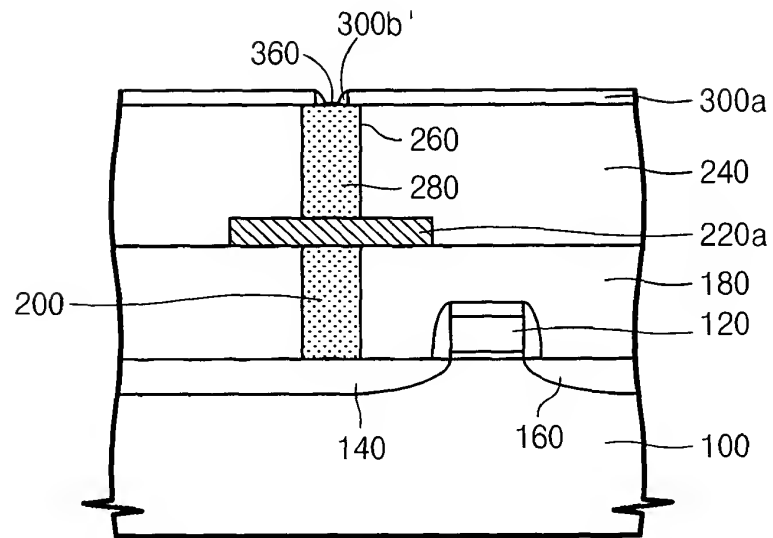


Fig. 9D

